ET850

AMD Athlon™ II / Turion™ II Neo CPU
785E + SB820M
COM Express (Type II) CPU Module

USER'S MANUAL

Version 1.0A

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Table of Contents

Table of Contents	iii
Introduction	1
Product Description	1
Checklist	
ET850 Specifications	3
Dimensions	
Installing the Memory	5
Jumpers and Connectors on ET850	
JB1: Clear CMOS Setting	
J2, J3: COM Express Type 2 Connectors	
COM Express Type 2 Connectors	7
BIOS Setup	9
Drivers Installation	29
VGA Drivers Installation	30
Audio Drivers Installation	35
LAN Drivers Installation	
Marvell LAN Drivers Installation (IP401-B1 carrier board only)	38





The ET850 COM Express CPU Module

Introduction

Product Description

The ET850 COM Express Module comes on board with the AMD Athlon II Neo or AMD TurioT II Neo processors and powered by the AMD 785E + SB820M chipset. The chipset has built-in Radeon HD4200 graphics engine with enhanced operating modes to enable excellent graphics performance in power and embedded applications. The

DirectX® 10.1 feature lets you enjoy awesome graphics performance, stunning 3D visual effects and dynamic Interactivity.

The board has one DDR3-800 SO-DIMM socket supporting up to 4GB of system memory. ET850 supports high speed connectivity with two SATA III, four serial ports, eight USB and a Gigabit LAN controller. Dimensions of the CPU module are 95mm x 125mm.

ET850 Features

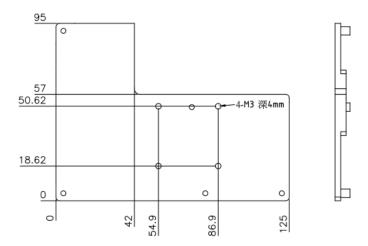
- AMD Athlon™ II Neo / Turion™ II Neo Processors onboard, up to 2.2GHz
- 1x DDR3-800/1333 SO-DIMM, Max. 4GB
- Integrated VGA, supports CRT & LVDS
- · Watchdog timer, HD Audio
- 2x SATA II, 1x GbE, 8x USB 2.0, 4x COM via baseboard

Checklist

Your ET850 package should include the items listed below.

- The ET850 CPU Module
- Heat spreader for ET850
- This User's Manual
- 1 CD containing the following:
 - Chipset Drivers
 - Flash Memory Utility

Remarks: After installing the heat spreader (provided with the CPU module), please install an additional heat spreader for better heat dissipation.

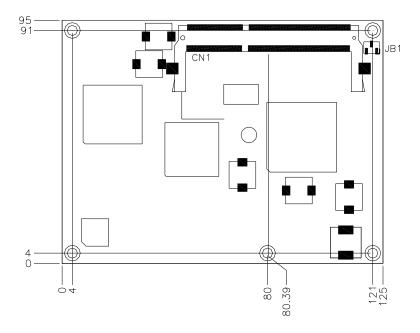




ET850 Specifications

Product Name	ET850
Form Factor	COM Express CPU module
CPU Type	AMD Geneva ASB2 Turion [™] II Neo / Athlon [™] II Neo DC
	CPU
CPU	Dual-Core CPU (27 x 27 mm) /45nm SOI / ECC capable
Operate	FSB up to 3200 MHz Hyper Transport
Frequency	AMD Athlon TM II Neo N36L=1.3GHz DC (12W) [ET850-13]
	AMD Turion TM II Neo N54L=2.2GHz DC (25W) [ET850-22]
	AMD Turion [™] II Neo N54H=2.2GHz DC (25W)
Cache	[ET850-22H]
CPU Socket	812-ball BGA ASB2 CPU on board
	AMD 785E NB : 21 mm x 21 mm
Chipset	AMD SB820M SB: 21 mm x 21 mm
BIOS	AMI BIOS
Memory	DDRIII-800 SO-DIMM x1 , Max. 4GB (Non-ECC) ** Please note N54H can support to 1333MHz**
VGA	AMD 785E built-in ATi HD4200 Graphics Core
VGA	CRT w/ DF13 connector (via internal RAM DAC)
LVDS	AMD 785E built-in 1 x 24-bit dual channels w/ DF13 socket
2400	x2 (via LVTM)
LAN	Realtek 8111DL PCI-Express GbE x 1
USB	SB820M built-in USB 2.0 host controller, supports 8 ports
Serial ATA Ports	SB820M built-in controller, supports 2 ports for SATA 3.0 (6
	Gb/s)
Parallel IDE	JMicron JM368 (PCI-e to PATA) x1 for 1 PATA channel for IDE
Audio	SB820M Built-in Audio controller + HD Codec ALC662 w/ 6
	channels (Line-out, Line-in, Mic.)
RTC	SB820M built-in RTC with on board battery
Watch-Dog Timer	Yes (256 segments, 0, 1, 2255. sec/min)
Connector	Two 220-pin connectors (A-B & C-D)
to Carrier Board	[COM Express 2.0 standard]
Power	+5V, +3.3V, +12V ,+5VSB
Other	LAN Wakeup
RoHS	Yes
Board Size	95mm x 125mm

Dimensions



Remarks: After installing the heat spreader (provided with the CPU module), please install an additional heat spreader for better heat dissipation.

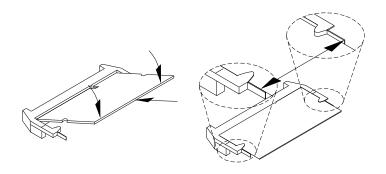
Installing the Memory

The ET850 COM Express CPU module accommodates 240-pin DDR3 SODIMM memory modules with capacities up to 4GB. Non-ECC is supported.

Installing and Removing Memory Modules

To install the DDR3 modules, locate the memory slot on the board and perform the following steps:

- 1. Hold the DDR3 module so that the key of the DDR3 module align with those on the memory slot. Insert the module into the socket at a slight angle (approximately 30 degrees). Note that the socket and module are both keyed, which means that the module can be installed only in one direction.
- To seat the memory module into the socket, apply firm and even pressure to each end of the module until you feel it slip down into the socket.
- 3. With the module properly seated in the socket, rotate the module downward. Continue pressing downward until the clips at each end lock into position.
- 4. To remove the DDR3 module, press the clips with both hands.



Jumpers and Connectors on ET850

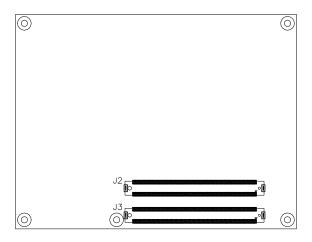
JB1: Clear CMOS Setting

JB1	Setting
123	Normal
123	Clear CMOS

Note: With jumper pin 1-2 short, it automatically saves the last BIOS settings when battery is removed, but it is not case with jumper pin 2-3 short.

J2, J3: COM Express Type 2 Connectors

The Type 2 connectors come in two 220-pin 0.5mm pitch receptacles. They include PCI, IDE, GBE and up to 22 general-purpose PCIE lanes (PCIE 0-5 and PCIE 16-31). For most Type 2 implementations, it is expected that PCIE lanes 16-31 are used for graphics. Hence they are designated PEG lanes 0-15 in the following table. Modules implementing Pin out Type 2, such as the ET850, uses the pin-out shown.



COM Express Type 2 Connectors

	Row A		Row B		Row C		Row D
Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
A1	GND (FIXED)	B1	GND (FIXED)	C1	GND (FIXED)	D1	GND (FIXED)
A2	GBE0 MDI3-	B2	GBE0 ACT#	C2	IDÈ D7	D2	IDÈ D5
А3	GBE0 MDI3+	B3	LPC FRAME#	C3	IDE D6	D3	IDE D10
A4	GBE0 LINK100#	B4	LPC AD0	C4	IDE D3	D4	IDE D11
A5	GBE0_LINK1000 #	B5	LPC_AD1	C5	IDE_D15	D5	IDE_D12
A6	GBE0 MDI2-	В6	LPC AD2	C6	IDE D8	D6	IDE D4
A7	GBE0 MDI2+	B7	LPC AD3	C7	IDE D9	D7	IDE_D0
A8	GBE0 LINK#	B8	LPC DRQ0#	C8	IDE D2	D8	IDE REQ
A9	GBE0 MDI1-	B9	LPC DRQ1#	C9	IDE D13	D9	IDE IOW#
A10	GBE0 MDI1+	B10	LPC CLK	C10	IDE D1	D10	IDE ACK#
A11	GND (FIXED)	B11	GND (FIXED)	C11	GND (FIXED)	D11	GND (FIXED)
A12	GBE0 MDI0-	B12	PWRBTN#	C12	IDE D14	D12	IDE IRQ
A13	GBE0 MDI0+	B13	SMB CK	C13	IDE IORDY	D13	IDE A0
A14	GBE0 CTREF	B14	SMB DAT	C14	IDE IOR#	D14	IDE A1
A15	SUS S3#	B15	SMB ALERT#	C15	PCI PME#	D15	IDE A2
A16	SATA0 TX+	B16	SATA1 TX+	C16	PCI GNT2#	D16	IDE_CS1#
A17	SATA0 TX-	B17	SATA1 TX-	C17	PCI REQ2#	D17	IDE CS3#
A18	NC	B18	SUS-STAT#	C18	PCI GNT1#	D18	IDE RESET#
A19	SATA0 RX+	B19	SATA1 RX+	C19	Pcl REQ1#	D19	PCI GNT3#
A20	SATA0 RX-	B20	SATA1 RX-	C20	PCI GNT0#	D20	PCI_REQ3#
A21	GND (FIXED)	B21	GND (FIXED)	C21	GND (FIXED)	D21	GND (FIXED)
A22	NC	B22	NC	C22	PCI REQ0#	D22	PCI AD1
A23	NC	B23	NC	C23	PCI RESET#	D23	PCI AD3
A24	SUS S5#	B24	PWR OK	C24	PCI AD0	D24	PCI AD5
A25	NC	B25	NC	C25	PCI AD2	D25	PCI AD7
A26	NC	B26	NC	C26	PCI_AD4	D26	PCI_C/BE0#
A27	BATLOW#	B27	WDT	C27	PCI_AD6	D27	PCI_AD9
A28	SATA_ACT#	B28	HDA_SDIN2	C28	PCI_AD8	D28	PCI_AD11
A29	HDA_SYNC	B29	HDA_SDIN1	C29	PCI_AD10	D29	PCI_AD13
A30	HDA_RST#	B30	HDA_SDIN0	C30	PCI_AD12	D30	PCI_AD15
A31	GND (FIXED)	B31	GND (FIXED)	C31	GND (FIXED)	D31	GND (FIXED)
A32	HDA_BITCLK	B32	SPKR	C32	PCI_AD13	D32	PCI_PAR
A33	HDA_SDOUT	B33	I2C_CK	C33	PCI_C/BE1#	D33	PCI_SERR#
A34	BIOS_DIS0#	B34	I2C_DAT	C34	PCI_PERR#	D34	PCI_STOP#
A35	THRMTRIP#	B35	THRM#	C35	PCI_LOCK#	D35	PCI_TRDY#
A36	USB6-	B36	USB7-	C36	PCI_DEVSEL#	D36	PCI_FRAME#
A37	USB6+	B37	USB7+	C37	PCI_IRDY#	D37	PCI_AD16
A38	USB_6_7_OC#	B38	USB_4_5_OC#	C38	PCI_C/BE2#	D38	PCI_AD18
A39	USB4-	B39	USB5-	C39	PCI_AD17	D39	PCI_AD20
A40	USB4+	B40	USB5+	C40	PCI_AD19	D40	PCI_AD22
A41	GND (FIXED)	B41	GND (FIXED)	C41	GND (FIXED)	D41	GND (FIXED)
A42	USB2-	B42	USB3-	C42	PCI_AD21	D42	PCI_AD24
A43	USB2+	B43	USB3+	C43	PCI_AD23	D43	PCI_AD26
A44	USB_2_3_OC#	B44	USB_0_1_OC#	C44	PCI_C/BE3#	D44	PCI_AD28
A45	USB0-	B45	USB1-	C45	PCI_AD25	D45	PCI_AD30
A46	USB0+	B46	USB1+	C46	PCI_AD27	D46	PCI_IRQC#
A47	VCC_RTC	B47	EXCD1_PERTST#	C47	PCI_AD29	D47	PCI_IRQD#
A48	EXCD0_PERST#	B48	EXCD1_CPPE#	C48	PCI_AD31	D48	PCI_CLKRUN#
A49	EXCD0CPPE#	B49 B50	SYS_RESET#	C49	PCI_IRQA#	D49 D50	NC PCI CLK
A50	LPC_SERIRQ	ROU	CB_RESET#	C50	PCI_IRQB3	טפע	PCI_CLK

ASS		Row A		Row B		Row C		Row D
ASS	Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
A53	A51	GND (FIXED)	B51	GND (FIXED)	C51	GND (FIXED)	D51	GND (FIXED)
A55	A52	PCIE_TX5+	B52	PCIE_RX5+	C52	PEG_RX0+	D52	PEG_TX0+
ASS								
A56	A54	GPI0	B54	GPO1	C54	NC		PEG_LANE_RV#
AST	A55	PCIE_TX4+	B55	PCIE_RX4+	C55	PEG_RX1+	D55	PEG_TX1+
ASB								
ASS								
A60								
A61 PCIE TX2+ B61 PCIE RX2+ C61 PEG RX3+ D61 PEG TX3- A62 PCIE TX2- B62 PCIE RX2- C62 PEG RX3- D62 PEG TX3- A63 GPI1 B63 GPO3 C63 RSVD D63 RSVD A64 PCIE TX1+ B64 PCIE RX1+ C64 RSVD D64 RSVD A65 PCIE TX1+ B65 PCIE RX1+ C65 PEG RX4+ D65 PEG TX4- A66 GND B66 WAKE0# C66 PEG RX4- D66 PEG TX4- A66 GND B66 WAKE0# C66 PEG RX4- D66 PEG TX4- A67 GPI2 B67 WAKE1# C67 RSVD D67 GND A68 PCIE TX0- B68 PCIE RX0- C68 PEG RX5- D68 PEG TX5- A69 PCIE TX0- B69 PCIE RX0- C68 PEG RX5- D68 PEG TX5- A70 GND (FIXED) B70 GND (FIXED) A71 LVDS A0+ B71 LVDS B0+ C71 PEG RX6- D71 PEG TX9- A72 LVDS A0- B72 LVDS B0- C72 PEG RX6- D71 PEG TX9- A73 LVDS A1- B73 LVDS B1- C74 PEG RX7- D74 PEG TX7- A74 LVDS A1- B73 LVDS B2- C75 PEG RX7- D74 PEG TX7- A75 LVDS A2- B76 LVDS B2- C76 GND D76 GND A77 LVDS VDD EN B77 LVDS B3+ C77 RSVD D77 IDE CBILD# A78 LVDS A3- B78 LVDS B3- C78 PEG RX8- D79 PEG TX7- A76 LVDS A3- B78 LVDS B3- C77 PEG RX8- D79 PEG TX7- A77 LVDS A0- B71 LVDS B3- C75 PEG RX7- A78 LVDS A3- B78 LVDS B3- C76 GND D76 GND A77 LVDS A3- B78 LVDS B3- C77 RSVD D77 IDE CBILD# A78 LVDS A3- B78 LVDS B3- C77 RSVD D77 IDE CBILD# A79 LVDS A3- B78 LVDS B3- C78 PEG RX8- D79 PEG TX8- A80 GND (FIXED) B80 GND (FIXED) B90 GND (FIXED)			_					
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A82			B81		C81		D81	
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A86	A84	LVDS_I2C_DAT	B84		C84	GND	D84	GND
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A93 GPO0 B93 VGA_HSYNC C93 GND D93 GND A94 SPI_CLK B94 VGA_VSYNC C94 PEG_RX13+ D94 PEG_TX13+ A95 SPI_MOSI B95 VGA_I2C_CK C95 PEG_RX13- D95 PEG_TX13- A96 GND B96 VGA_I2C_CATA C96 GND D96 GND A97 NC B97 SPI_CS# C97 RSVD D97 PEG_ENABLE# A98 RSVD B98 RSVD C98 PEG_RX14+ D98 PEG_TX14+ A99 RSVD B99 RSVD C99 PEG_RX14+ D99 PEG_TX14+ A100 GND (FIXED) C100 GND (FIXED) D100 GND (FIXED) A101 RSVD B101 RSVD C101 PEG_RX15+ D101 PEG_TX15+ A102 RSVD B102 RSVD C102 PEG_RX15- D102 PEG_TX15+ A103 RSVD <td></td> <td></td> <td>_</td> <td></td> <td></td> <td></td> <td></td> <td></td>			_					
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A95								
A96								
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A99 RSVD B99 RSVD C99 PEG_RX14- D99 PEG_TX14- A100 GND (FIXED) B100 GND (FIXED) C100 GND (FIXED) D100 GND (FIXED) GND GND (FIXED) D100 GND (FIXED) GND GND (FIXED) GND GND (FIXED) GND GND (FIXED) D101 PEG_TX15- D102 RSVD C102 PEG_RX15- D102 PEG_TX15- D101 PEG_TX15-								
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A102 RSVD B102 RSVD C102 PEG_RX15- D102 PEG_TX15- A103 RSVD B103 RSVD C103 GND D103 GND A104 VCC_12V B104 VCC_12V C104 VCC_12V D104 VCC_12V A105 VCC_12V B105 VCC_12V C105 VCC_12V D105 VCC_12V A106 VCC_12V B106 VCC_12V C106 VCC_12V D106 VCC_12V A107 VCC_12V B107 VCC_12V C107 VCC_12V D107 VCC_12V A108 VCC_12V B108 VCC_12V C108 VCC_12V D108 VCC_12V A109 VCC_12V B109 VCC_12V C109 VCC_12V D109 VCC_12V								
A103								
A104 VCC_12V B104 VCC_12V C104 VCC_12V D104 VCC_12V A105 VCC 12V B105 VCC 12V C105 VCC 12V D105 VCC 12V A106 VCC 12V B106 VCC 12V C106 VCC 12V D106 VCC 12V A107 VCC 12V B107 VCC_12V C107 VCC 12V D107 VCC_12V A108 VCC 12V B108 VCC 12V C108 VCC 12V D108 VCC 12V A109 VCC_12V B109 VCC_12V C109 VCC_12V D109 VCC_12V			_					
A105 VCC_12V B105 VCC_12V C105 VCC_12V D105 VCC_12V A106 VCC_12V B106 VCC_12V C106 VCC_12V D106 VCC_12V A107 VCC_12V B107 VCC_12V C107 VCC_12V D106 VCC_12V A108 VCC_12V B108 VCC_12V C108 VCC_12V D108 VCC_12V A109 VCC_12V B109 VCC_12V C109 VCC_12V D109 VCC_12V								
A106 VCC_12V B106 VCC_12V C106 VCC_12V D106 VCC_12V A107 VCC_12V B107 VCC_12V C107 VCC_12V D107 VCC_12V A108 VCC_12V B108 VCC_12V C108 VCC_12V D108 VCC_12V A109 VCC_12V B109 VCC_12V C109 VCC_12V D109 VCC_12V		_					_	_
A107 VCC_12V B107 VCC_12V C107 VCC_12V D107 VCC_12V A108 VCC_12V B108 VCC_12V C108 VCC_12V D108 VCC_12V A109 VCC_12V B109 VCC_12V C109 VCC_12V D109 VCC_12V								
A108 VCC_12V B108 VCC_12V C108 VCC_12V D108 VCC_12V A109 VCC_12V B109 VCC_12V C109 VCC_12V D109 VCC_12V								
A109 VCC_12V B109 VCC_12V C109 VCC_12V D109 VCC_12V								
ATTO GND (FIXED) B110 GND (FIXED) C110 GND (FIXED) D110 GND (FIXED)	A110	GND (FIXED)	B110	GND (FIXED)	C110	GND (FIXED)	D110	GND (FIXED)

BIOS Setup

This chapter describes the different settings available in the AMI (American Megatrends, Inc.) BIOS that comes with the board. The topics covered in this chapter are as follows:

BIOS Introduction	10
BIOS Setup	10
Main BIOS Setup	11
Advanced Settings	
PCIPnP Settings	19
Boot Settings	
Security Settings	22
Advanced Chipset Settings	23
Exit Setup	28
Save Changes and Exit	
Discard Changes and Exit	28
Discard Changes	28
Load Optimal Defaults	28
Load Failsafe Defaults	28

BIOS Introduction

The BIOS provides critical low-level support for a standard device such as disk drives, serial ports and parallel ports. It also adds virus and password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

BIOS Setup

The BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn on the computer, the BIOS is immediately activated. Pressing the key immediately allows you to enter the Setup utility. If you are a little bit late pressing the key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup. If you still wish to enter Setup, restart the system by pressing the "Reset" button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system Off and back On again. The following message will appear on the screen:

Press to Enter Setup

In general, you press the arrow keys to highlight items, <Enter> to select, the <PgUp> and <PgDn> keys to change entries, <F1> for help and <Esc> to quit.

When you enter the Setup utility, the Main Menu screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.

Main BIOS Setup

This setup allows you to record some basic hardware configurations in your computer system and set the system clock.

BIOS SETUP UTILITY

Main	Advanced	PCIPnP	Boot	Security	/	Chipset	Exit
System	Overview			_		ENTER], [-
AMIBIOS						SHIFT-TAB ct a field.	j to
Version	:08.00.15				00.0	ot a mora.	
Build Date	9:09/15/10					[+] or [-] to	
Processor	r				con	igure syst	em Time.
AMD Turio	on™ II Neo N54	L Dual Core P	rocessor				
Speed	: 2200MHz						
Count	: 2				<-	Select Sc	reen
System M	emory				↑↓ +-	Select Ite Change F	
Size	: 1792MB				Tab	Select Fie	eld
					F1	General H	Help
System Ti		-	7:00:00]		F10	Save and	Exit
System Da	ite	רן	hu 09/13/2010)]	ESC	Exit	

Note: If the system cannot boot after making and saving system changes with Setup, the AMI BIOS supports an override to the CMOS settings that resets your system to its default.

Warning: It is strongly recommended that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both AMI and your system manufacturer to provide the absolute maximum performance and reliability. Changing the defaults could cause the system to become unstable and crash in some cases.

Advanced Settings

This section allows you to configure and improve your system and allows you to set up some system features according to your preference.

BIOS SETUP UTILITY

Main	Advanced	PCIPnP	Boot	Secur	ity	Chipset	Exit
Adva	nced Settings				Conf	igure CPU.	
WARNI	NG: Setting wrong may cause s						
► IDE Co ► Super I ► ACPI C ► AHCI C ► Hardwa ► PCI Ex ► USB Co ► Lan Co	onfigurations Infiguration O Configuration Configuration Configuration are Health Configuration press Configuration onfiguration Infiguration Configuration Configuration					Select Scre Select Item r Go to General He Save and E Exit	Sub Screen

The fields in each section are shown in the following sections, as seen in the computer screen. Please note that setting the wrong values may cause the system to malfunction. If unsure, please contact technical support of your supplier.

Advanced			
CPU Configuration Module Version: 15.08 AGESA Version: 1.0.0.0 Physical Count: 1 Logical Count: 2		This option should remain disabled for the normal operation. The driver develop may enable it for testing purpose.	er
AMD Turion™ II Neo N54L Dual Core Pro Revision: C3 Cache L1: 256KB	ocessor		
Cache L2: 2048KB Cache L3: N/A Speed: 2200MHz, NB Clk: 1600MHz Able to Change Freq. : Yes uCode Patch Level: 0x10000B6		<- Select Screen	
GART Error Reporting Microcode Update Secure Virtual Machine Mode PowerNow C1E Support	[Disabled] [Enabled] [Enabled] [Enabled] [Enabled]	↑↓ Select Item +- Change Field F1 General Help F10 Save and Exit ESC Exit	

Advanced		
IDE Configuration		DISABLED: disables the integrated IDE Controller.
OnBoard PCI IDE Controller Primary IDE Master Primary IDE Slave	[Both] : [Not Detected] : [Not Detected]	PRIMARY: enables only the Primary IDE Controller. SECONDARY: enables only the Secondary IDE Controller.
 ▶ Secondary IDE Master ▶ Secondary IDE Slave ▶ Third IDE Master ▶ Third IDE Slave 	: [Not Detected] : [Not Detected] : [Not Detected] : [Not Detected]	BOTH: enables both IDE Controllers.
➤ Fourth IDE Master ➤ Fourth IDE Slave	: [Not Detected] : [Not Detected]	 <- Select Screen ↑↓ Select Item +- Change Field
Hard Disk Write Protect IDE Detect Time Out (Sec)	[Disabled] [35]	F1 General Help F10 Save and Exit
ATA(PI) 80Pin Cable Detection	[Host & Device]	ESC Exit

The IDE Configuration menu is used to change and/or set the configuration of the IDE devices installed in the system.

Advanced		
Configure Win627EHF Su Floppy A Serial Port1 Address Serial Port2 Address Serial Port2 Mode Parallel Port1 Address Parallel Port Mode	[Disabled] [3F8/IRQ4] [2F8/IRQ3] [Normal] [378] [Normal]	Allows BIOS to Select Serial Port Base Addresses <- Select Screen ↑↓ Select Item +- Change Field
Parallell Port IRQ Restore on AC Power Loss Power On function	[IRQ7] [Power Off] [None]	F1 General Help F10 Save and Exit ESC Exit

Onboard Serial Port

The default values are:

Serial Port 1: 3F8/IRQ4 Serial Port 2: 2F8/IRQ3

Restore on AC Power Loss

This field sets the system power status whether *Power On or Power Off* when power returns to the system from a power failure situation.

Advanced		
Configure Secondary Fin Chipset	itek F81216D Super IO	Allows BIOS to Select Serial Port Base
Serial Port3 Address Serial Port3 Mode Serial Port4 Address Serial Port4 Mode	[3E8] [5] [2E8] [10]	Addresses <- Select Screen ↑↓ Select Item +- Change Field F1 General Help F10 Save and Exit ESC Exit

Advanced	
ACPI Settings	General ACPI Configuration settings
► General ACPI Configuration ► Advanced ACPI Configuration	

BIOS SETUP UTILITY

Advanced		
General ACPI Configuration		Select the ACPI state used for
Suspend mode C1E Support	[S1 (POS)] [Enable]	System Suspend.

BIOS SETUP UTILITY

Advanced		
Advanced ACPI Configuration		Enable RSDP pointers to 64-bit Fixed System
ACPI Version Features	[ACPI v1.0]	Description Tables.
ACPI APIC support	[Enabled]	Different ACPI version
AMI OEMB table Headless mode	[Enabled] [Disabled]	Has some addition

Advanced		
AHCI Settings	[Enabled]	Enables for supporting AHCI controller in AHCI
AHCI BIOS Support		mode during BIOS control otherwise operates in IDE
ALIOL Desto	Dist Data da di	mode.
AHCI Port0	[Not Detected]	
AHCI Port1	[Not Detected]	
AHCI Port2	[Not Detected]	
AHCI Port3	[Not Detected]	
AHCI Port4	[Not Detected]	
AHCI Port5	[Not Detected]	

Advanced		
Hardware Health Configur	ration	Options
System Temperature CPU Temperature	:75°C/167°F :78°C/172°F	Disabled 80°C/176°F 85°C/185°F
CPU_VDD_RUN CPU_VDDR +3.3V +5V VCC	:1.148V :0.902V :3.260V :4.933 V :4.914 V	90°C/194°F 95°C/203°F
5VSB	:4.914 V :4.872 V	<- Select Screen
CPU Shutdown Temperature	[Disabled]	↑↓ Select Item+- Change Field
		Tab Select Field F1 General Help
		F10 Save and Exit ESC Exit

Advanced		
PCI Express Configuration	Enables/Disables Pci Express Device	
Relaxed Ordering	[Auto]	Relaxed Ordering.
Maximum Payload Size	[Auto]	
Extended Tag Field No Snoop Maximum Read Reqquest Size Active State Power Management Extended Synch	[Auto] [Auto] [Auto] [Disabled] [Auto]	

Advanced		
USB Configuration	USB Configuration	
Module Version - 2.24.5-13.4		
USB Devices Enabled: 1 Keyboard, 1 Mouse, 1 Drive		
Legacy USB Support USB 2.0 Controller Mode BIOS EHCI Hand-Off Legacy USB1.1 HC Support ►USB Mass Storage Device Confi	[Enabled] [HiSpeed] [Enabled] [Enabled]	 Select Screen ↑↓ Select Item +- Change Field F1 General Help F10 Save and Exit ESC Exit

The USB Configuration menu is used to read USB configuration information and configure the USB settings.

Legacy USB Support

Enables support for legacy USB. AUTO option disables legacy support if no USB devices are connected.

USB 2.0 Controller Mode

Configures the USB 2.0 controller in HiSpeed (480Mbps) or FullSpeed (12Mbps). This option is enabled by HiSpeed.

BIOS EHCI Hand-Off

Enabled/Disabled. This is a workaround for Oses without EHCI hand-off support. The EHCI ownership change should be claimed by EHCI driver.

Legacy USB1.1 HC Support Support USB1.1 HC.

BIOS SETUP UTILITY

	BIOS SETOF CHEITT		
Advanced			
Lan Configuration		Options	
Onboard LAN Option ROM	[Disabled]	Disabled Enabled	

	Disable/Enable RTC to generate
[Disabled]	a wake event.
[Disabled]	
[Disabled]	
	[Disabled]

PCIPnP Settings

This option configures the PCI/PnP settings.

BIOS SETUP UTILITY

Main	Advanced	PCIPnP	Boot	Security	y Chipset	Exit
Adva	nced PCI/PnI	P Settings			NO: lets the Bl	os
	<u> </u>				Configure all th	ne .
WARN	ING: Setting wro				Devices in the	system.
	may cause	system to malfi	anction.		YES: lets the	
Clear NV	/PAM		[No]		operating syste	em
Plug & F			[No]		configure Plug	and
PCI Late	ncy Timer		[64]		Play (PnP) devi	
	IRQ to PCI VGA		[Yes]		required for bo	
Palette S	Snooping BusMaster		[Disabled] [Enabled]		•	
	PCI/ISA IDE Car	d	[Auto]		your system ha	•
					and Play opera	ting
IRQ3			[Available]		system.	
IRQ4			[Available]			
IRQ5			[Reserved]			
IRQ7			[Available]			
IRQ9			[Available]			
IRQ10			[Reserved]			
IRQ11			[Available]			
IRQ14			[Available]			
IRQ15			[Available]			
					<- Select Sc	reen
DMA Ch			[Available]		↑↓ Select Iter	
DMA Ch			[Available]		+- Change F	ield
DMA Ch			[Available]		F1 General H	lelp
DMA Ch			[Available]		F10 Save and	Exit
DMA Ch			[Available]		ESC Exit	
DMA Ch	annel 7		[Available]			
Reserve	d Memory Size		[Disabled]			

Clear VRAM

Clear VRAM during system boot.

Plug & Play O/S

This lets BIOS configure all devices in the system or lets the OS configure PnP devices not required for boot if your system has a Plug and Play OS.

Allocate IRQ to PCI VGA

This assigns IRQ to PCI VGA card if card requests IRQ or doesn't assign IRQ to PCI VGA card even if card requests an IRQ.

Palette Snooping

When enabled, PCI will allow VGA palette signals to go to the ISA bus.

PCI IDE BusMaster

This function allows the BIOS to use PCI BusMastering for reading or writing to IDE drives.

OffBoard PCI/ISA IDE Card

This option specifies if an offboard PCI IDE controller adapter card is installed in the computer. You must specify the PCI Expansion slot on the motherboard where the offboard PCI IDE controller is installed. This disables the onboard PCI IDE controller. You must also specify the IRQs for this PCI IDE card.

IRQ#

Use the IRQ# address to specify what IRQs can be assigned to a particular peripheral device.

Boot Settings

Main	Advanced	PCIPnP	Boot	Security	Chipset	Exit
Boot	Settings				Configure Settings during System Bo	
► Boot I	Settings Configu Device Priority Disk Drives VD Drives	ıration			 Select Screenth Select Item Change Field 	n I ub Screen

	Boot	
Boot Settings Configuration		Allows BIOS to skip certain tests while booting. This will
Quick Boot	[Enabled]	decrease the time
Quiet Boot	[Disabled]	needed to boot the
AddOn ROM Display Mode	[Force BIOS]	system.
Bootup Num-Lock	[On]	
PS/2 Mouse Support	[Auto]	<- Select Screen
Wait for 'F1' If Error	[Enabled]	↑↓ Select Item
Hit 'DEL' Message Display	[Enabled]	+- Change Field
Interrupt 19 Capture	[Disabled]	F1 General Help
		F10 Save and Exit
		ESC Exit

Quick Boot

This allows BIOS to skip certain tests while booting. This will decrease the time needed to boot the system.

Quite Boot

When disabled, this displays normal POST messages. When enabled, this displays OEM Logo instead of POST messages.

AddOn ROM Display Mode

This allows user to force BIOS/Option ROM of add-on cards to be displayed during quiet boot.

Bootup Num-Lock

This select the power-on state for numlock.

PS/2 Mouse Support

This select support for PS/2 mouse.

Wait for 'F1' If Error

When set to Enabled, the system waits for the F1 key to be pressed when error occurs. This allows option ROM to trap interrupt 19.

Hit Message Display

This displays "Press to run Setup" in POST.

Interrupt 19 Capture

This allows option ROMs to trap interrupt 19.

Security Settings

This setting comes with two options set the system password. Supervisor Password sets a password that will be used to protect the system and Setup utility. User Password sets a password that will be used exclusively on the system. To specify a password, highlight the type you want and press <Enter>. The Enter Password: message prompts on the screen. Type the password and press <Enter>. The system confirms your password by asking you to type it again. After setting a password, the screen automatically returns to the main screen.

To disable a password, just press the <Enter> key when you are prompted to enter the password. A message will confirm the password to be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

Main	Advanced	PCIPnP	Boot	Security	Chipset	Exit
Secu	rity Settings				tall or Chan	ge the
Superv	isor Password :	Not Installed				
User P	assword :	Not Installed				
Chang	e Supervisor Pas	ssword		<-	Select Sc	reen
Change	e User Password			↑↓ Ent	Select Iter	
Boot Se	ector Virus Protec	tion [Disabled]		F1	General H	•
				F10	Save and	Exit
				ES	C Exit	

Advanced Chipset Settings

This setting configures the north bridge, south bridge and the ME subsystem. WARNING! Setting the wrong values may cause the system to malfunction.

BIOS SETUP UTILITY

Main	Advanced	PCIPnP	Boot	Security	Chipset	Exit
Advanced Chipset Settings					Options for NB	
► Nort	rth Bridge Con h Bridge2 Configu th Bridge Configur	ration			<- Select Scre ↑↓ Select Item Enter Go to F1 General He F10 Save and E ESC Exit	sub Screen

		Chipset	
North Bridge Chipset C	North Bridge Chipset Configuration		
► Memory Configuration ► DRAM Timing Configuration			
Size of Dimm #0: 1 GB Size of Dimm #1: Non-Presence	<- Select Screen		
Memory CLK CAS Latency(Tcl) RAS/CAS dELAY(Trcd) Row Precahrge Time (Trp) Min Active RAS (Tras) RAS/RAS Delay (Trrd) Row Cycle (Trc) Read to Precharge (Trtp) Write Recover Time (Twr)	: 15 CLK , N/A : 4 CLK , N/A : 20 CLK , N/A : 4 CLK , N/A	↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit	
HT Link Width Control GfxNBPstateDis Support T0Time Override	[Enable] [Enable] [Disabled]		

Memory Configuration BIOS SETUP UTILITY

		Chipset
Memory Configuration Channel Interleaving Enable Clock to All DIMMs Memory Hole Remapping CS Sparing Enable Power Down Enable Power Down Mode DRAM Parity Enable Bank Swizzle Mode	[Auto] [Disabled] [Enabled] [Disabled] [Auto] [Auto] [Auto] [Auto]	Enable Channel Memory Interleaving <- Select Screen ↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit

DRAM Timing Configuration BIOS SETUP UTILITY

	Chipset
DRAM Timing Configuration	Optons
DRAM Timing Config [Auto]	Auto Manual
	<- Select Screen
	↑↓ Select Item Enter Go to Sub Screen
	F1 General Help
	F10 Save and Exit
	ESC Exit

NorthBridge2 Chipset Configuration BIOS SETUP UTILITY

		Chipset
NorthBridge2 Chipset Configura	NorthBridge2 Chipset Configuration	
RS880 CIMx Version: 1.3.0.5		
► Internal Graphics Configuration		
NB Power Management Features Memory Hole	[Auto] [Disabled]	<- Select Screen ↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit

Internal Graphics Configuration BIOS SETUP UTILITY

		Chipset
Internal Graphics Configuration	1	Options
Internal Graphics Mode UMA Frame Buffer Size SIDEPORT Clock Speed GFX Engine Clock Override UMA-SP Interleave Mode SP Power Management SP NB Termination SP Memory Termination SP CMD Hold	[UMA+SIDEPORT] [Auto] [400MHz] [Disable] [Auto] [Auto] [Disable] [Disable] [Disable] [Auto]	Disable UMA SIDEPORT UMA+SIDEPORT <- Select Screen
SP CMD Hold	[Auto]	↑↓ Select Item Enter Go to Sub Screen
Special Graphics Features FB Location	[Disabled] [Below 4G]	F1 General Help F10 Save and Exit ESC Exit
PANEL ID Selection	[1024 x 768 24 bit]	

South Bridge Configuration BIOS SETUP UTILITY

	Chipset
SouthBridge Chipset Configuration	Options for SB GPP Por
► SP GPP Port Graphics Configuration ► SB Azalia Audio Configuration ► SB SATA Configuration	
	<- Select Screen
	↑↓ Select Item Enter Go to Sub Screen
	F1 General Help
	F10 Save and Exit
	ESC Exit

BIOS SETUP UTILITY

	Chipset	
SB GPP Port Configuration	Options	
SB GPP Fort Configuration SB GPP Function GPP Port Link Configuration Unhide unused GPP ports GPP Link ASPM GPP Lane Reversal NB-SB PHY PLL Power Down GPP PHY PLL Power Down GPP Link Down [Enable] [Enable] [Enable]		Disable Enable <- Select Screen ↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit
	(2.00.0)	1 1 0 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1

	Chipset
Onchip HD Azalia Configuration HD Audio Azalia Device [Enabled HD Onboard PIN Config [Enabled Azalia Front Panel [Auto] [Azalia] SDIN1 Pin Config [Azalia] SDIN1 Pin Config [Azalia] SDIN2 Pin Config [Azalia] SDIN3 Pin Config [GPIO] Azalia Snoop [Disabled]	Options Auto Disable Enable <- Select Screen ↑ Select Item Enter Go to Sub Screen

		Chipset
Onchip SATA Configuration		Options
OnChip SATA Channel OnChip SATA Type OnChip IDE Type SATA IDE Combined Mode PATA Channel Config	[Enabled] [IDE] [Legacy IDE] [Enabled] [SATA as primary]	Auto Disable Enable <- Select Screen ↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit

OnChip SATA Type

The options are:

- (1) IDE
- (2) RAID
- (3) AHCI

Exit Setup

The exit setup has the following settings which are:

BIOS SETUP UTILITY

Main	Advanced	PCIPnP	Boot	Security	Chipset	Exit
Exit Options				Exit system setup after saving the		
Save (Changes and E	xit			changes.	
Discar	d Changes and	Exit				
Discar	Discard Changes				10 key can b	e used
				1	or this operat	tion
Load C	Optimal Default	S				
Load F	ailsafe Default	s			<- Select S	creen
	Load Falloare Boldano				↑↓ Select Ito Enter Go	em to Sub Screen
					F1 General	Help
					F10 Save and	d Exit
					ESC Exit	

Save Changes and Exit

This option allows you to determine whether or not to accept the modifications and save all changes into the CMOS memory before exit.

Discard Changes and Exit

This option allows you to exit the Setup utility without saving the changes you have made in this session.

Discard Changes

This option allows you to discard all the changes that you have made in this session.

Load Optimal Defaults

This option allows you to load the default values to your system configuration. These default settings are optimal and enable all high performance features.

Load Failsafe Defaults

This option allows you to load the troubleshooting default values permanently stored in the BIOS ROM. These default settings are non-optimal and disable all high-performance features.

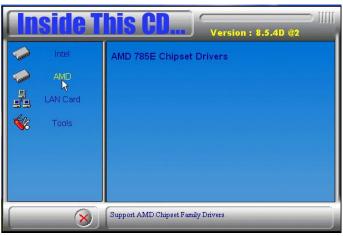
Drivers Installation

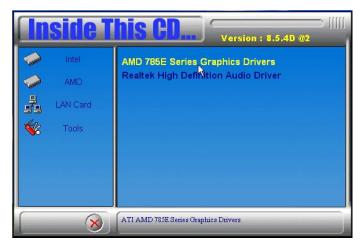
This section describes the installation procedures for software and drivers under the Windows XP and Windows Vista. The software and drivers are included with the board. If you find the items missing, please contact the vendor where you made the purchase. The contents of this section include the following:

VGA Drivers Installation	30
Audio Drivers Installation	35
LAN Drivers Installation	36
Marvell LAN Drivers Installation (IP401-B1 carrier board only)	38

VGA Drivers Installation

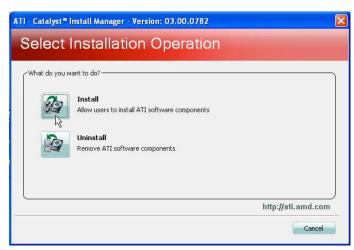
1. Insert the CD that comes with the board. Click *AMD* then *AMD 785E Chipset Drivers* and then *AMD 785E Series Graphics Drivers*.





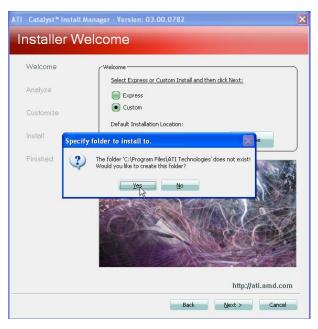
2. When the Welcome Screen appears, click *Next*. Click *Install* to install the ATI software components.

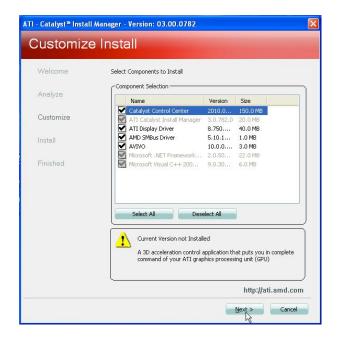




3. Click *Custom* and select the components to install as shown.

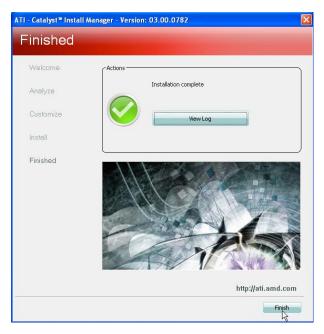






4. Accept the license agreement to proceed with installation. Reboot the computer when prompted for changes to take effect.

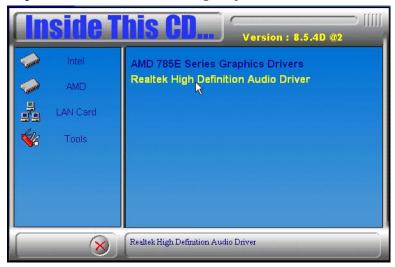






Audio Drivers Installation

1. Insert the CD that comes with the board. Click *AMD* then *AMD* 785E Chipset Drivers and then Realtek High Definition Audio Driver.



- 2. The Welcome screen to the InstallShieled Wizard for Realtek High Definition Audio Driver will appear. At this point, click **Next** to continue the installation process.
- 3. When installation is completed, restart the computer as prompted. Click **Finish**.

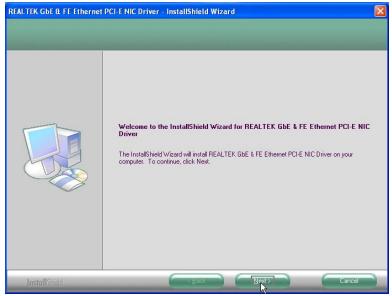
LAN Drivers Installation

1. Insert the CD that comes with the board. Click *LAN Card* at the left side and then *Realtek LAN Controller Drivers*.





2. In the welcome screen of the InstallShield Wizard for REALTEK GbE & FE Ethernet PCI-E NIC Driver, click *Next*.



- 3. In the InstallShield Wizard screen, click *Install* to begin the installation.
- 4. InstallShield Wizard completed. Click *Finish* to exit the Wizard.

Marvell LAN Drivers Installation (IP401-B1 carrier board only)

Follow the steps below to complete the installation of the Intel PRO LAN drivers.

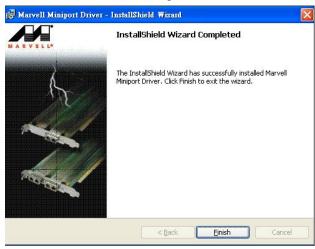
1. Insert the CD that comes with the board. Click *LAN Card* and then *Marvell LAN Controller Driver*.



2. When the Welcome screeen appears, click *Next* to continue.



- 3. Click *Next* to agree with the license agreement.
- 4. When the Readme Information appears, click *Next* to continue
- 5. When the Ready to Install the Program appears, click *Install* to continue.
- 6. After the installation is complete, click *Finish*.



- 7. To use the wake up function with PCIe LAN, go to the *Device Manager under Windows* and select *LAN controller*. The window for *Generic Marvell Yukon Chipset based Ethernet Controller Properties* will appear. Click *Advanced* and select *Wake From Shutdown*. In the Value field on the right, select *On*.
- 8. Then, also in the *Advanced* section, click on *Wake Up Capabilities*. In the Value field on the right, select *Magic Packet*, then click *OK*.

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